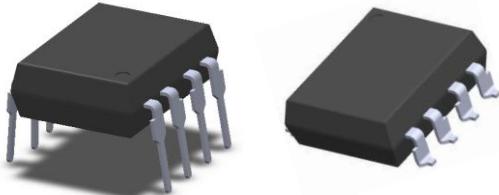


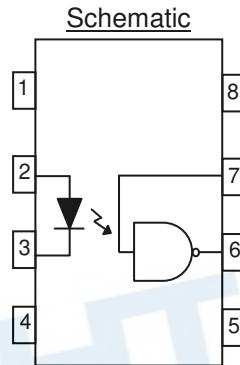
DATASHEET

8 PIN DIP WIDE BODY HIGH SPEED 10MBit/s LOGIC GATE PHOTOCOUPLED ELW137 ELW26XX Series



Features

- High speed 10Mbit/s
- Guaranteed performance from -40 to 85°C
- Logic gate output
- High isolation voltage between input and output ($V_{iso} = 5000$ V rms)
- Compliance with EU REACH
- The product itself will remain within RoHS compliant version
- UL and cUL approved(No. E214129)
- VDE approved (No. 40028391)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved



A 0.1 μ F bypass capacitor must be connected between pins 8 and 5 ³

Pin Configuration
 1, No Connection
 2, Anode
 3, Cathode
 4, No Connection
 5, Gnd
 6, V_{out}
 7, V_E
 8, V_{cc}

Description

The ELW137, ELW2601 and ELW2611 consists of an infrared emitting diode optically coupled to a high speed integrated photo detector logic gate with a strobeable output.

It is packaged in a 8-pin wide body package and available SMD options.

Applications

- Ground loop elimination
- LSTTL to TTL, LSTTL or 5 volt CMOS
- Line receiver, data transmission
- Data multiplexing
- Switching power supplies
- Pulse transformer replacement
- Computer peripheral interface
- High speed logic ground isolation

Truth Table (Positive Logic)

| Input | Enable | Output |
|-------|--------|--------|
| H | H | L |
| L | H | H |
| H | L | H |
| L | L | H |
| H | NC | L |
| L | NC | H |

Absolute Maximum Ratings (T_A=25°C)

| | Parameter | Symbol | Rating | Unit |
|--------|--|------------------|------------|-------|
| Input | Forward current | I _F | 50 | mA |
| | Enable input voltage Not exceed V _{CC} by more than 500mV | V _E | 5.5 | V |
| | Reverse voltage | V _R | 5 | V |
| Output | Power dissipation | P _D | 100 | mW |
| | Power dissipation | P _C | 85 | mW |
| | Output current | I _O | 50 | mA |
| | Output voltage | V _O | 7.0 | V |
| | Supply voltage | V _{CC} | 7.0 | V |
| | Isolation voltage *1 | V _{ISO} | 5000 | V rms |
| | Operating temperature | T _{OPR} | -40 ~ +85 | °C |
| | Storage temperature | T _{STG} | -55 ~ +125 | °C |
| | Soldering temperature *2 | T _{SOL} | 260 | °C |

Notes:

*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2, 3 & 4 are shorted together, and pins 5, 6, 7 & 8 are shorted together.

*2 For 10 seconds.

Electrical Characteristics ($T_A=-40$ to 85°C unless specified otherwise)**Input**

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Condition |
|--|-------------------------|------|------|------|----------------------------|---|
| Forward voltage | V_F | - | 1.4 | 1.8 | V | $I_F = 10\text{mA}$ |
| Reverse voltage | V_R | 5.0 | - | - | V | $I_R = 100\mu\text{A}$, $T_A=25^\circ\text{C}$ |
| Temperature coefficient of forward voltage | $\Delta V_F/\Delta T_A$ | - | -1.9 | - | $\text{mV}/^\circ\text{C}$ | $I_F=10\text{mA}$ |
| Input capacitance | C_{IN} | - | 70 | - | pF | $V_F=0$, $f=1\text{MHz}$ |

Output

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Condition |
|--|-----------|------|------|------|------|--|
| High level supply current | I_{CCH} | - | 6.5 | 10 | mA | $I_F=0\text{mA}$, $V_E=0.5\text{V}$, $V_{CC}=5.5\text{V}$ |
| Low level supply current | I_{CCL} | - | 8 | 13 | mA | $I_F=10\text{mA}$, $V_E=0.5\text{V}$, $V_{CC}=5.5\text{V}$ |
| High level enable current | I_{EH} | - | -0.6 | -1.6 | mA | $V_E=2.0\text{V}$, $V_{CC}=5.5\text{V}$ |
| Low level enable current | I_{EL} | - | -0.8 | -1.6 | mA | $V_E=0.5\text{V}$, $V_{CC}=5.5\text{V}$ |
| High level enable voltage | V_{EH} | 2.0 | - | - | V | $I_F=10\text{mA}$, $V_{CC}=5.5\text{V}$ |
| Low level enable voltage ^{*4} | V_{EL} | - | - | 0.8 | V | $I_F=10\text{mA}$, $V_{CC}=5.5\text{V}$ |

Transfer Characteristics ($T_A=-40$ to 85°C unless specified otherwise)

| Parameter | Symbol | Min | Typ. | Max. | Unit | Condition |
|---------------------------|----------|-----|------|------|------|--|
| High level output current | I_{OH} | - | 2.1 | 100 | uA | $V_{CC}=5.5\text{V}$, $V_O=5.5\text{V}$, $I_F=250\mu\text{A}$, $V_E=2.0\text{V}$ |
| Low level output voltage | V_{OL} | - | 0.35 | 0.6 | V | $V_{CC} = 5.5\text{V}$, $I_F=5\text{mA}$, $V_E=2.0\text{V}$, $I_{OL}(\text{Sinking})=13\text{mA}$ |
| Input threshold current | I_{FT} | - | 3.0 | 5 | mA | $V_{CC}= 5.5\text{V}$, $V_O=0.6\text{V}$, $V_E=2.0\text{V}$, $I_{OL}(\text{Sinking})=13\text{mA}$ |

Switching Characteristics ($T_A=-40$ to 85°C , $V_{CC}=5\text{V}$, $I_F=7.5\text{mA}$ unless specified otherwise)

| Parameter | Symbol | Min | Typ. | Max. | Unit | Condition |
|--|-----------------------|-----|------|------|------|--|
| Propagation delay time to output High level ^{*5} (Fig.12) | T_{PHL} | - | 35 | 100 | ns | $C_L = 15\text{pF}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ |
| Propagation delay time to output Low level ^{*6} (Fig.12) | T_{PLH} | - | 40 | 100 | ns | $C_L = 15\text{pF}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ |
| Pulse width distortion | $ T_{PHL} - T_{PLH} $ | - | 5 | 40 | ns | $C_L = 15\text{pF}$, $R_L = 350\Omega$ |
| Output rise time ^{*7} (Fig.12) | tr | - | 40 | - | ns | $C_L = 15\text{pF}$, $R_L = 350\Omega$ |
| Output fall time ^{*8} (Fig.12) | tf | - | 10 | - | ns | $C_L = 15\text{pF}$, $R_L = 350\Omega$ |

Switching Characteristics ($T_A=-40$ to 85°C , $V_{CC}=5\text{V}$, $I_F=7.5\text{mA}$ unless specified otherwise)

| Parameter | Symbol | Min | Typ. | Max. | Unit | Condition |
|---|-----------------|--------|------|------|------------|---|
| Enable Propagation Delay Time to Output High Level ^{*9} (Fig.13) | t_{ELH} | - | 15 | - | ns | $I_F = 7.5\text{mA}$, $V_{EH} = 3.5\text{V}$, $C_L = 15\text{pF}$, $R_L = 350\Omega$ |
| Enable Propagation Delay Time to Output Low Level ^{*10} (Fig.13) | t_{EHL} | - | 15 | - | ns | $I_F = 7.5\text{mA}$, $V_{EH} = 3.5\text{V}$, $C_L = 15\text{pF}$, $R_L = 350\Omega$ |
| ELW137 | | - | - | - | | $I_F = 7.5\text{mA}$, $V_{OH} = 2.0\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 10\text{Vp-p}$ (Fig.14) |
| Common Mode Transient Immunity at Logic High ^{*11} | ELW2601 | 5,000 | - | - | V/ μ S | $I_F = 7.5\text{mA}$, $V_{OH} = 2.0\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 50\text{Vp-p}$ (Fig.14) |
| ELW2611 | CM _H | | | | | $I_F = 7.5\text{mA}$, $V_{OH} = 2.0\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 400\text{Vp-p}$ (Fig.14) |
| ELW2611 | | 10,000 | - | - | | $I_F = 7.5\text{mA}$, $V_{OH} = 2.0\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 400\text{Vp-p}$ (Fig.15) |
| ELW137 | | - | - | - | | $I_F = 0\text{mA}$, $V_{OL} = 0.8\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 10\text{Vp-p}$ (Fig.14) |
| Common Mode Transient Immunity at Logic Low ^{*12} | ELW2601 | 5,000 | - | - | V/ μ S | $I_F = 0\text{mA}$, $V_{OL} = 0.8\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 50\text{Vp-p}$ (Fig.14) |
| ELW2611 | CM _L | | | | | $I_F = 0\text{mA}$, $V_{OL} = 0.8\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 400\text{Vp-p}$ (Fig.14) |
| ELW2611 | | 10,000 | - | - | | $I_F = 7.5\text{mA}$, $V_{OH} = 2.0\text{V}$, $R_L = 350\Omega$, $T_A = 25^\circ\text{C}$ $V_{CM} = 400\text{Vp-p}$ (Fig.15) |
| ELW2611 | | 20,000 | - | - | | |

Typical Electro-Optical Characteristics Curves

Figure 1. Forward Current vs Forward Voltage

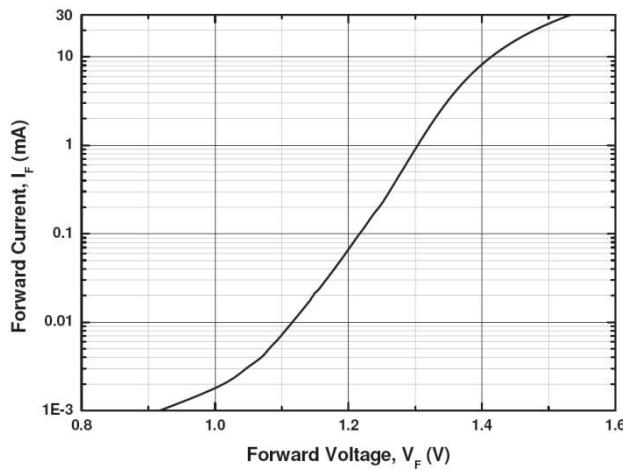


Figure 3. Low Level Output Current vs Ambient Temperature

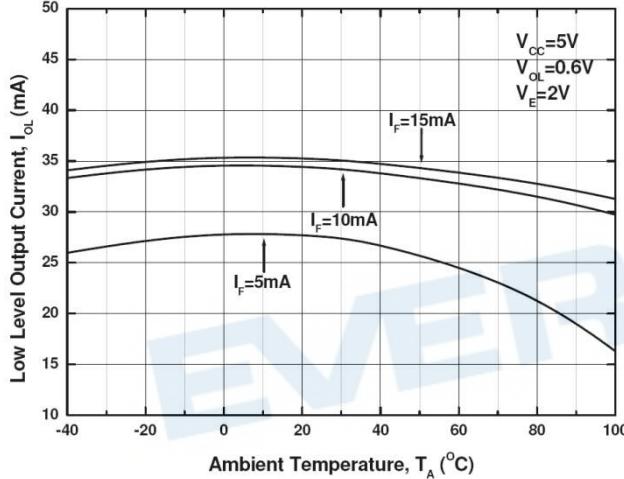


Figure 5. Input Current vs Output Voltage

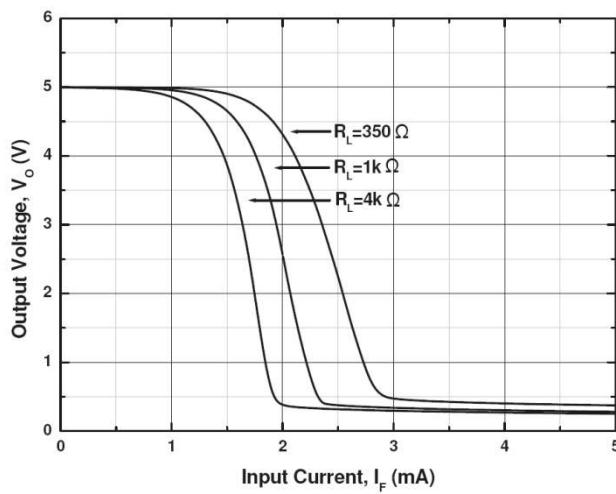


Figure 2. Low Level Output Voltage vs Ambient Temperature

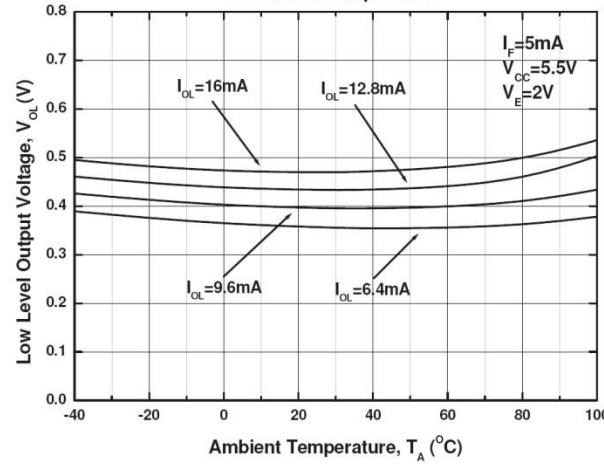


Figure 4. Input Threshold Current vs Ambient Temperature

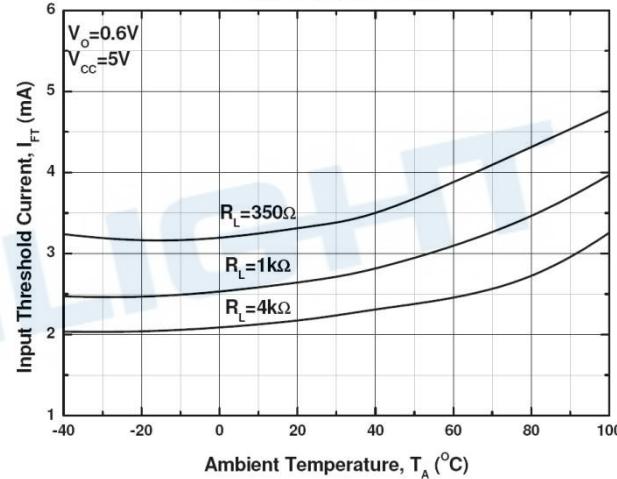


Figure 6. High Level Output Current vs Ambient Temperature

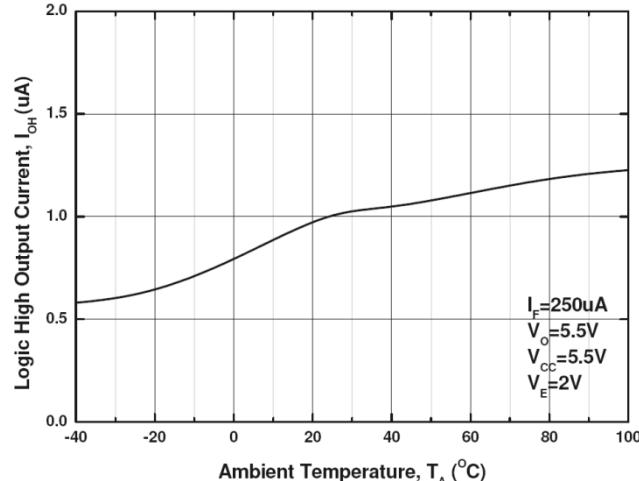


Figure 8. Propagation Delay vs. Temperature

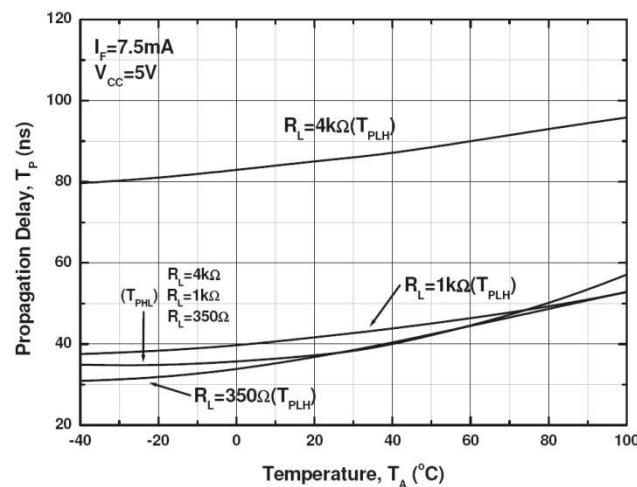


Figure 9. Pulse Width Distortion vs. Temperature

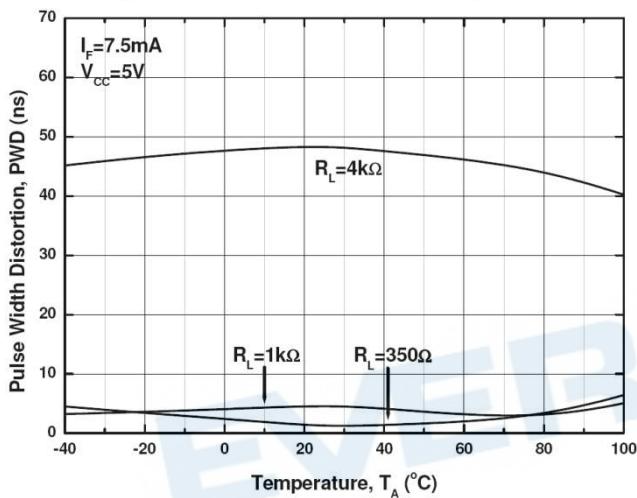


Figure 10. Rise and Fall Time vs. Temperature

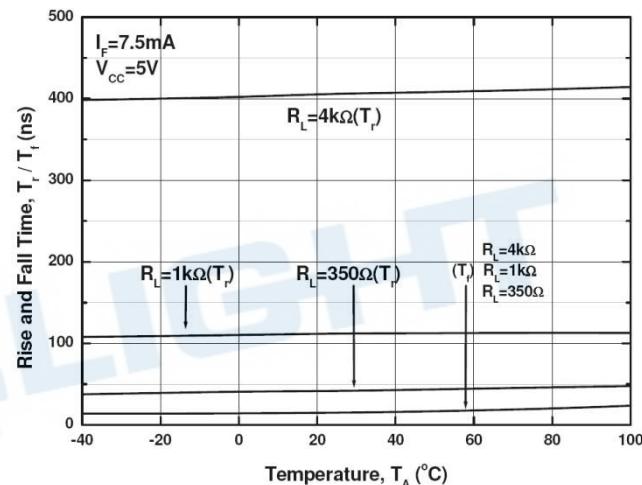


Figure 11. Enable Propagation Delay vs. Temperature

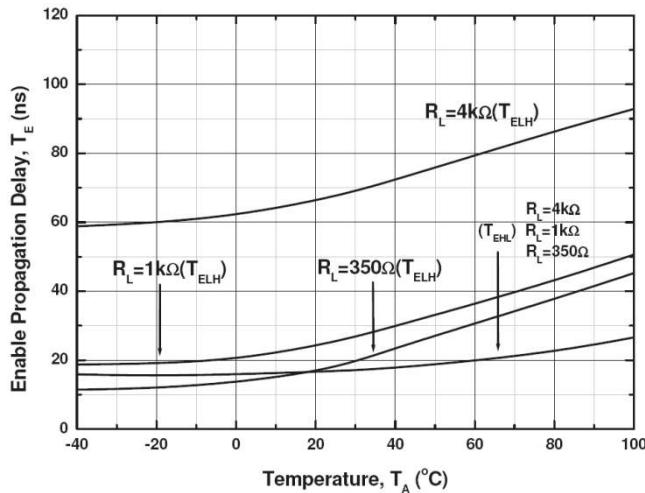


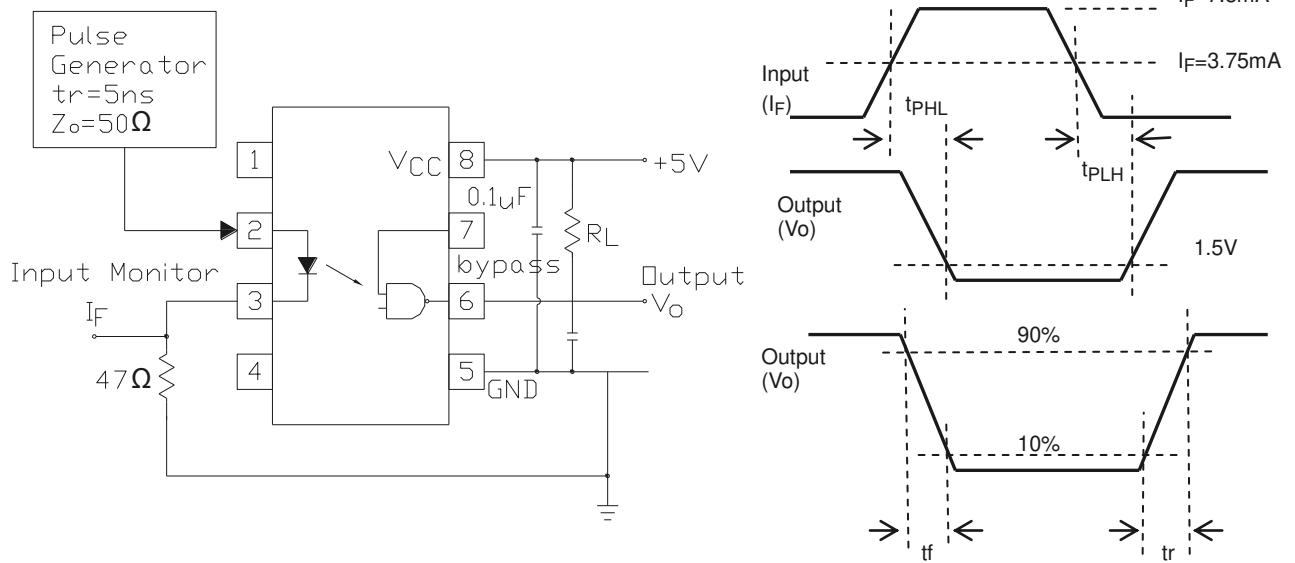
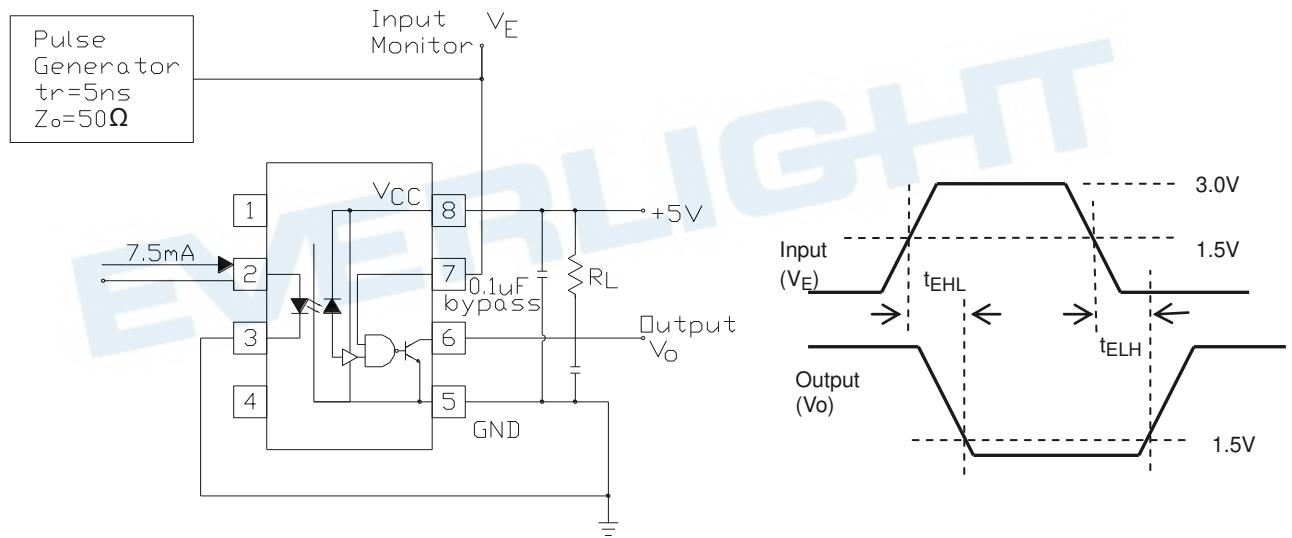
Fig. 12 Test Circuit and Waveforms for T_{PHL} , T_{PLH} , tr , and tf **Fig. 13 Test Circuit and Waveform for t_{EHL} and t_{ELH}** 

Fig. 14 Test Circuit Common Mode Transient Immunity

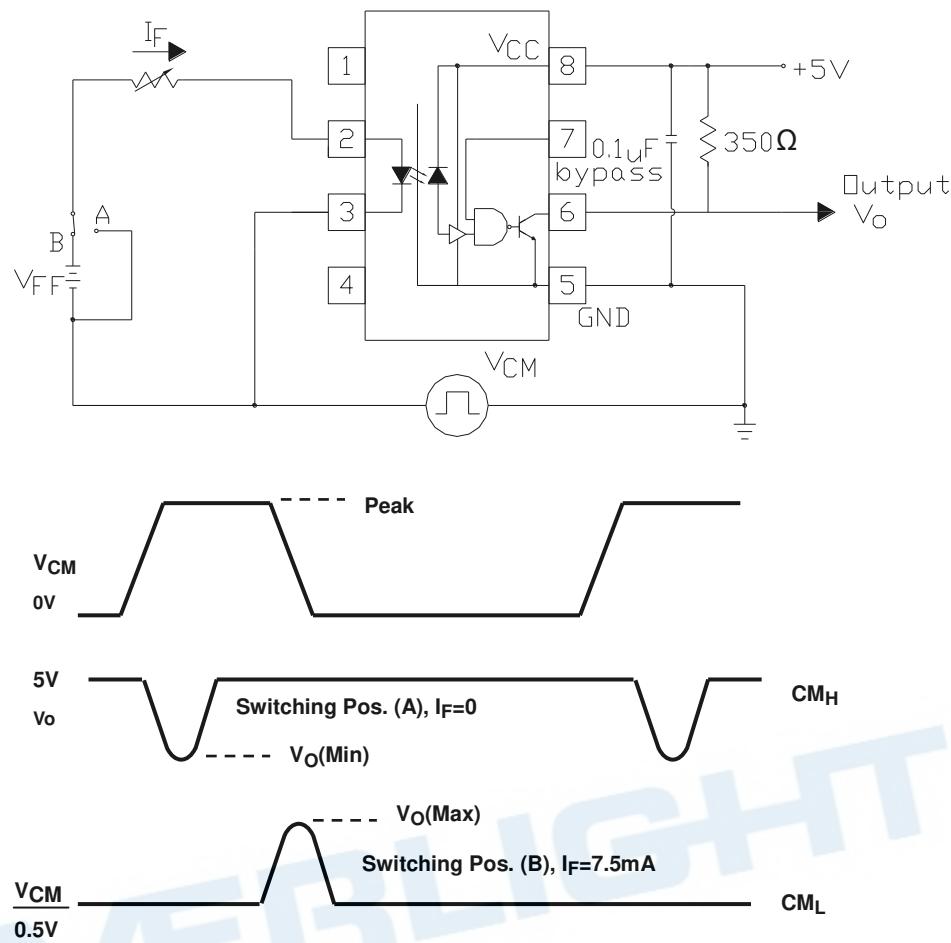
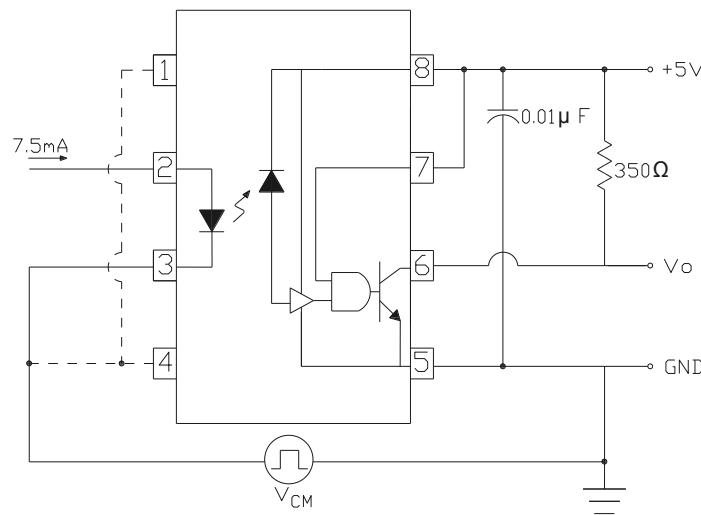


Fig. 15 Recommended Drive Circuit for ELW2611 Families for High-CMR



Note

- *3 The V_{CC} supply must be bypassed by a $0.1\mu F$ capacitor or larger. This can be either a ceramic or solid tantalum capacitor with good high frequency characteristic and should be connected as close as possible to the package V_{CC} and GND pins
- *4. Enable Input – No pull up resistor required as the device has an internal pull up resistor.
- *5. T_{PLH} – Propagation delay is measured from the $3.75mA$ level on the HIGH to LOW transition of the input current pulse to the 1.5 V level on the LOW to HIGH transition of the output voltage pulse.
- *6. T_{PHL} – Propagation delay is measured from the $3.75mA$ level on the LOW to HIGH transition of the input current pulse to the 1.5 V level on the HIGH to LOW transition of the output voltage pulse.
- *7. tr – Rise time is measured from the 90% to the 10% levels on the LOW to HIGH transition of the output pulse.
- *8. tf – Fall time is measured from the 10% to the 90% levels on the HIGH to LOW transition of the output pulse.
- *9. $tELH$ – Enable input propagation delay is measured from the $1.5V$ level on the HIGH to LOW transition of the input voltage pulse to the $1.5V$ level on the LOW to HIGH transition of the output voltage pulse.
- *10. $tEHL$ – Enable input propagation delay is measured from the $1.5V$ level on the LOW to HIGH transition of the input voltage pulse to the $1.5V$ level on the HIGH to LOW transition of the output voltage pulse.
- *11 CMH – The maximum tolerable rate of rise of the common mode voltage to ensure the output will remain in the HIGH state (i.e., $V_{OUT} > 2.0V$).
- *12 CML – The maximum tolerable rate of rise of the common mode voltage to ensure the output will remain in the LOW output state (i.e., $V_{OUT} < 0.8V$).

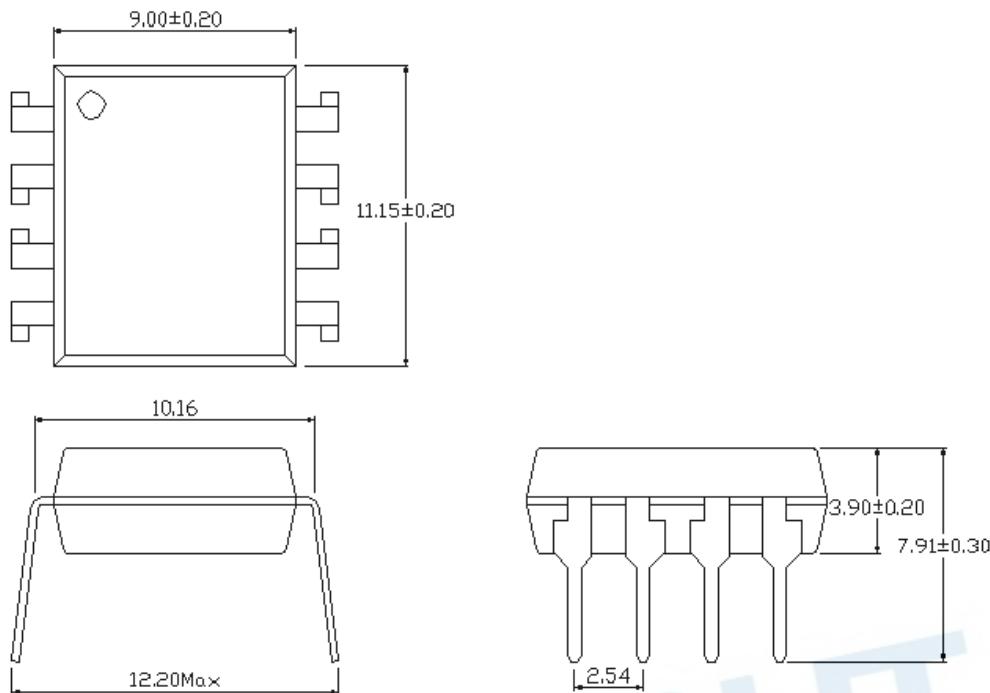
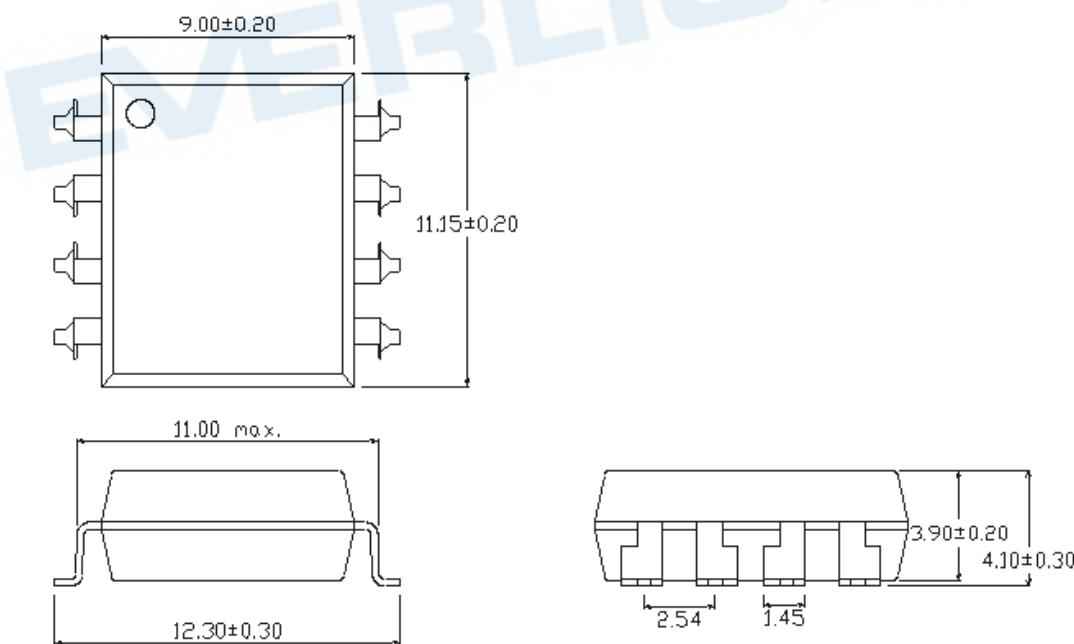
Order Information**Part Number****ELW137Y(Z)-V**

Or

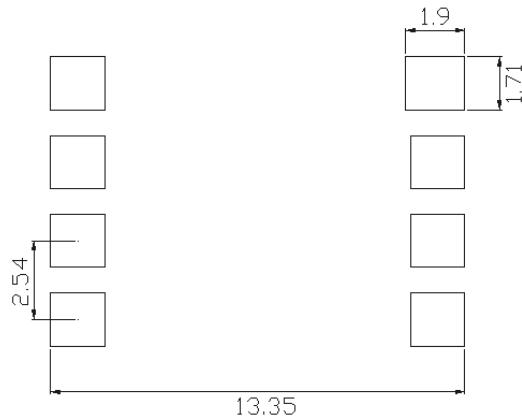
ELW26XXY(Z)-V**Note**

- XX = 01 or 11 for ELW26 part no.
- Y = Lead form option (S or none)
- Z = Tape and reel option (TA, TB or none)
- V = VDE (optional)

| Option | Description | Packing quantity |
|--------|---|--------------------|
| None | Standard DIP-8 | 40 units per tube |
| S (TA) | Surface mount lead form + TA tape & reel option | 500 units per reel |
| S (TB) | Surface mount lead form + TB tape & reel option | 500 units per reel |

Package Dimension
(Dimensions in mm)**Standard DIP Type****Option S Type**

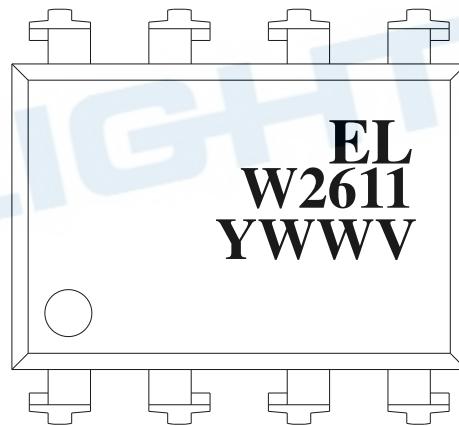
Recommended Pad Layout for Surface Mount Leadform



Notes

Suggested pad dimension is just for reference only.
Please modify the pad dimension based on individual need.

Device Marking

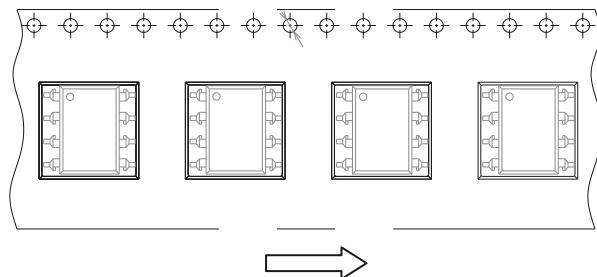


Notes

| | |
|-------|---------------------------|
| EL | denotes EVERLIGHT |
| W137 | denotes Device Number |
| W2611 | denotes Device Number |
| Y | denotes 1 digit Year code |
| WW | denotes 2 digit Week code |
| V | denotes VDE (optional) |

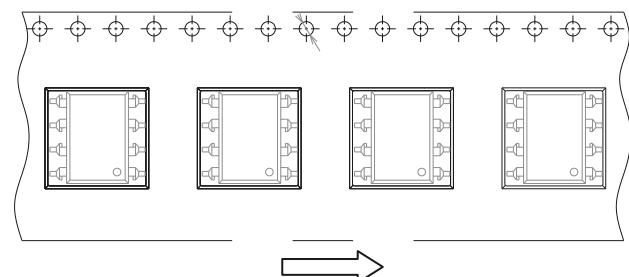
Tape & Reel Packing Specifications

Option TA



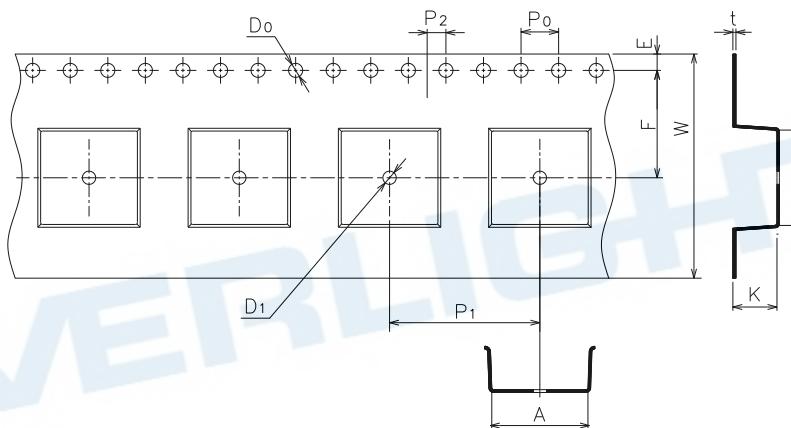
Direction of feed from reel

Option TB



Direction of feed from reel

Tape Dimension

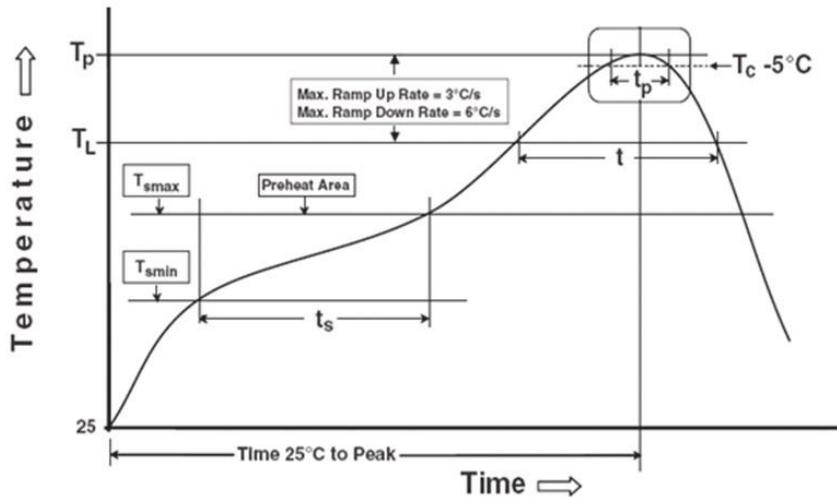


| Dimension No. | A | B | Do | D1 | E | F |
|---------------|----------------|-----------------|---------------|----------------|-----------------|----------------|
| Dimension(mm) | 12.7 ± 0.1 | 11.45 ± 0.1 | 1.5 ± 0.1 | 1.5 ± 0.1 | 1.75 ± 0.1 | 11.5 ± 0.1 |
| Dimension No. | P0 | P1 | P2 | t | W | K |
| Dimension(mm) | 4.0 ± 0.1 | 16.0 ± 0.1 | 2.0 ± 0.1 | 0.4 ± 0.05 | 24.00 ± 0.3 | 4.6 ± 0.1 |

Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Reference: IPC/JEDEC J-STD-020D

Preheat

| | |
|--|----------------|
| Temperature min (T_{smin}) | 150°C |
| Temperature max (T_{smax}) | 200°C |
| Time (T_{smin} to T_{smax}) (t_s) | 60-120 seconds |
| Average ramp-up rate (T_{smax} to T_p) | 3°C/second max |

Other

| | |
|---|------------------|
| Liquidus Temperature (T_L) | 217°C |
| Time above Liquidus Temperature (t_L) | 60-100 sec |
| Peak Temperature (T_p) | 260°C |
| Time within 5°C of Actual Peak Temperature: $T_p - 5°C$ | 30 s |
| Ramp- Down Rate from Peak Temperature | 6°C /second max. |
| Time 25°C to peak temperature | 8 minutes max. |
| Reflow times | 3 times |

DISCLAIMER

1. Above specification may be changed without notice. EVERLIGHT will reserve authority on material change for above specification.
2. The graphs shown in this datasheet are representing typical data only and do not show guaranteed values.
3. When using this product, please observe the absolute maximum ratings and the instructions for use outlined in these specification sheets. EVERLIGHT assumes no responsibility for any damage resulting from use of the product which does not comply with the absolute maximum ratings and the instructions included in these specification sheets.
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